

Requested document:

[JP2156227 click here to view the pdf document](#)**DISPLAY ELECTRODE SUBSTRATE OF ACTIVE MATRIX DISPLAY DEVICE**

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Abstract

PURPOSE: To eliminate a delay of a signal even when a disconnection defect is corrected by a spare wiring by making line width of an intersecting part with a bus line of the spare wiring narrower than line width of a non- intersecting part.

CONSTITUTION: As for a part W which does not intersect with each bus line X-Z of spare wirings 33a, 33b, its line width is formed widely in order to suppress small a wiring resistance of the spare wirings 33a, 33b. On the other hand, as for an intersecting part N with each bus line X-Z, its line width is formed more narrowly enough than the non-intersecting part W in order to suppress small the floating capacity generated between the spare wirings 33a, 33b and each bus line X-Z. In such a way, since a time constant being incidental to the spare wiring is suppressed small, even if a disconnection defect of the bus line is corrected by the spare wiring, no delay is generated in a signal supplied through its spare wiring.

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